

<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>	Atty. Docket No. 030086A	Serial No.: Not yet assigned
	Applicant(s): Shunji NAKAMURA	
	Filing Date: February 24, 2004	Group Art Unit: 2831 (Expected)

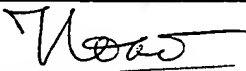
### U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
dh	AA	2002/0056869	Morimoto	05/16/2002	257	313	11/13/2001
	AB	2002/0155626	Park	10/24/2002	438	3	04/18/2002
	AC	6,552,384	Murata et al.	04/22/2003	257	307	07/02/2001
	AD	6,198,617	Sun	03/06/2001	361	301	01/12/1999
dh	AE	5,978,207	Anderson et al.	11/02/1999	361	311	10/30/1997
	AF						

### FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
dh	AG	11-97535	04/09/1999	Japan	Abstract
	AH				
	AI				
	AJ				
	AK				

### OTHER DOCUMENTS

dh	AL	Hiroshi HORIE et al.; International Electron Device Meeting, pp.946-948, 1996.
dh	AM	S. NAKAMURA et al.; Symposium on VLSI Technology Digest of Technical Papers, 4A-2, pp.35-36, 1999.
Examiner		<div style="display: flex; justify-content: space-between;"> <div>  </div> <div> Date Considered 08/18/2004 </div> </div>